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Substitute	e for form 1449A/PT()	TRAI STRAI	Complete if Known		
				Application Number	10/720,857	
			CLOSURE	Filing Date	November 24, 2003	
STA	TEMENT B	Y A	PPLICANT	First Named Inventor	Pravas Pradhan et al.	
				Group Art Unit	2816	
	(use as many she	ets as	necessary)	Examiner Name	Lam, Tuan Thieu	
Sheet	1	of	2	Attorney Docket Number	112055-0073U	

				U.S. PATENT DOCUME	NTS					
Examiner Initials *	Cits No.1	Cite No.1	Cits No.1	Cits No.1	Number (if kr	Code ²	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Co	lumns, Lines, Where Relevant assages or Relevant Figures Appear
7L	1	2003/01074	A1	Martin et al.	06-12-2003	3271	100			
W	2	5.767.699		Bosnyak et al.	06-16-1998	326	186			
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				FOREIGN	PATENT DOCU	MENTS		
Examine r Initials*		Foreign Patent Document			Name of Patentee or	Date of Publication of	Pages, Columns, Lines,	
	Cite No.1	Office ³	Number ⁴	Kind Code ⁶ (if known)	Applicant of Cited Document	Cited Document MM- DD-YYYY	Where Relevant Passages or Relevant Figures Appear	T _a
$\cdot \mathcal{N}$	3	WO	95/27353		Brunt et al.	10-12-1995		
/ N	4	EP	1017196	A2	Gabara	07-05-2000		
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Sheet 2 of 2

	Complete if Known	
Application Number	10/720,857	
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Attorney Docket Number	112055-0073U	

		OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS	
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γ	5	SIM ET AL. "A 1-Gb/s Bidirectional I/O Buffer Using the Current-Mode Scheme" IEE Journal of Solid-State Circuits, New York, US, April 1999.	
W	6	International Search Report for International Application No. PCT/US/2004/037145, dated June 29, 2005.	
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Examiner Signature	7	Molan	Date Considered	96/05
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